

IDC04S60C

2^{nd} generation thinQ!TM SiC Schottky Diode

FEATURES:

Applications:

- Revolutionary semiconductor material -Silicon Carbide
- Switching behavior benchmark
- No reverse recovery
- No temperature influence on the switching behavior
- No forward recovery
- High surge current capability

• SMPS, PFC, snubber



Chip Type	V_{BR}	I _F	Die Size	Package
IDC04S60C	600V	4A	1.146 x 0.968 mm ²	sawn on foil

MECHANICAL PARAMETER:

Raster size	1.146x 0.968	- mm			
Anode pad size	0.909 x 0.731				
Area total / active	1.11 / 0.74	mm ²			
Thickness	355	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	3461 pcs				
Passivation frontside	Photoimide				
Anode metalization	3200 nm Al				
Cathode metalization	talization 1400 nm Ni Ag –system suitable for epoxy and soft solder die bond				
Die bond	Electrically conductive glue or solder				
Wire bond	AI, ≤ 350μm				
Reject Ink Dot Size	Ø ≥ 0.3 mm				
Recommended Storage Environment store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°					



IDC04S60C

Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	V
DC blocking voltage	V_{DC}		600	1 '
Continuous forward current limited by T_{jmax}	I _F		4	
Surge non repetitive forward current sine halfwave	I _{F,SM}	$T_C = 25^{\circ}C, t_P = 10 \text{ ms}$	32	А
Repetitive peak forward current limited by T _{jmax}	I _{F,RM}	$T_C = 100$ °C, $T_j = 150$ °C, $D = 0.1$	18	
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25^{\circ}C$, $tp = 10\mu$ s	132	1
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-55+175	°C

Static Electrical Characteristics (tested on chip), T_i =25 °C, unless otherwise specified

Parameter	Symbol	Condi	Value			Unit	
r arameter	Cymbol	Condi	itions	min.	Тур.	max.	Oiiii
Reverse current	I_{R}	V _R =600V	<i>T_j</i> =25°C		0.5	50	μA
Diode forward voltage	V _F	I _F =4A	T _j =25°C		1.7	1.9	V

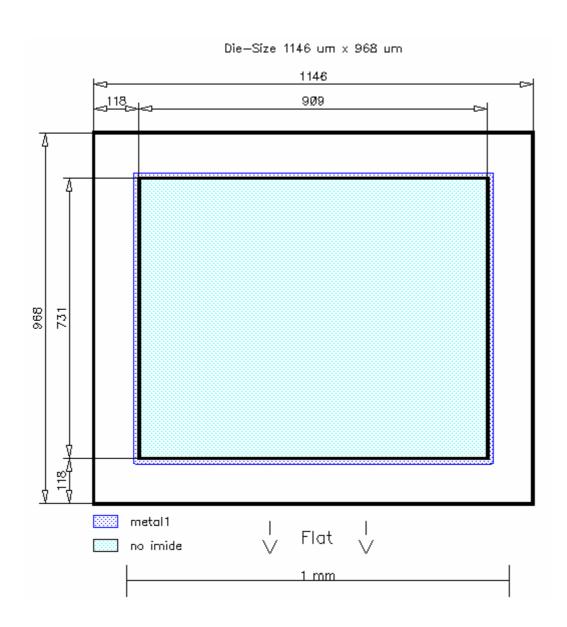
Dynamic Electrical Characteristics, at $T_i = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol	Conditions		Value			Unit
raiailletei	Syllibol			min.	Тур.	max.	
Total capacitive charge	Q_C	$I_F <= I_{F,max}$ di/dt = 200A/ms	$T_j = 150 ^{\circ}\mathrm{C}$		8		nC
Switching time 1)	t_c	V _R =400V	$T_j = 150 {}^{\circ}\text{C}$			<10	ns
Total capacitance	С	f=1MHz	V _R = 1 V		130		
			V _R =300V		20		pF
			V _R =600V		20		

 $^{^{1)}}$ t_{c} is the time constant for the capacitive displacement current waveform (independent from $T_{j},\ l_{LOAD}$ and di/dt), different from t_{rr} which is dependent on $T_{j},\ l_{LOAD}$ and di/dt. No reverse recovery time constant t_{rr} due to absence of minority carrier injection



CHIP DRAWING:





IDC04S60C

FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet INFINEON TECHNOLOGIES IDT04S60C

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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